Interfaces

Serial I/O Interface

The SIO 2-2 Serial I/O Interface board contains two identical ports, each permitting the computer to communicate with most peripheral devices through an RS232 or current loop interface. The two ports are independent. Each may operate through either the current loop or RS232 mode, and will operate in full-duplex or half-duplex with all control signals.

You can run synchronous or asynchronous lines, full- or halfduplex, at any baud rate up to 9600 baud (asynchronous) or 56,000 baud (synchronous). Baud rates up to 9600 (asynchronous) or 38,400 (synchronous) are selected by jumpers on the board. Asynchronous baud rates are 75, 110, 150, 300, 600, 1200, 2400, 4800 and 9600. Synchronous rates are 1200, 2400, 4800, 9600, 19,200 and 38,400. Other rates are made possible using the SIOC board which mounts directly on the SIO board.

Control lines for each input include DSR, DTR, RTS, CTS and Carrier Detect. RS232 receivers and drivers are also provided for clocks in synchronous operations. Jumpers permit using the board as either the receiving (terminal) end of a communication line or the originating (computer) end.

Each interface is structured around an Intel 8251 USART chip. This chip allows extensive program control of I/O functions including control line and sync character selection, and error-condition sensing and recovery. The board generates interrupts for received characters, transmitter buffer empty, transmitter empty or sync character.

The board may be jumper-adapted to respond either to I/O instructions from the I-8080 system or to memory reference instructions for memorymapped I/O.





"D" SERIES ENCLOSURE For 8" Rack Mount Drives

The dual 8" enclosure will hold two Shugart 801/851 R drives in a horizontal configuration. The power supply features full regulation, external heat sink and 3 amp continuous rating on both the +24VDC and the +5VDC outputs. The steel blue and grey cabinet has a 4" fan, multi-tap power connector/fuse holder, and a "Blue Ribbon" style 50 pin data connector.

ltem		Order	Price
Enclosu supply,	re, Power Cables	DE-8	240.
Above with 2 Shugart 800/801R Drives		DS-8	1100.
Above v CP/M 2 and Cor	with 2® ntroller	DT-8	1500.

 SIO-2-2
 2 serial I/O ports, good to 9600 baud . . \$175.00

 IEE 488+3
 \$375.00

IEEE-488 & 3P

The 488+3 is designed to be plugged into one slot of a standard IEEE-696 (S-100) cardcage. It consists of two major functional blocks:

- IEEE-488 Interface
- Three parallel ports

The IEEE-488 portion of the board is designed to perform the interface function between an IEEE 488-1975/78 General Purpose interface Bus (GPIB) and the CPU. It communicates with the CPU via an input/output-mapped 8-bit data bus and provides a 16-bit bus to interface with the GPIB via buffer devices. IEEE 488-1975/78 standard protocol is handled automatically in Talker, Listener, and Bus or System Controller operational modes. Its specific IEEE-488 features are:

- Handles all IEEE 488-1975/78
 functions
- Talker and listener functions (T, TE, L, LE)
- Automatic source and acceptor handshakes (SH, AH)
- Controller with pass control capabilities (C)
- System controller capabilities
- Device clear and trigger functions (DC, DT)
- Service request functions (SR)
- Parallel and serial poll facilities (PP, SP)
- Remote/local with local lockout (RL)
- Single or dual addressing modes
- Secondary addressing capabilities

The three parallel input/output ports portion of the board is designed to perform as a general purpose programmable I/O device. It has 24 I/O pins which may be individually programmed in two groups of twelve and used in three major modes of operation.

The 488+3's flexibility is enhanced by jumpers which allow the user to select input/output port addresses, interrupt priorities, etc.

The software I/O driver routines supplied with the 488+3 facilitate its use. These programs are callable subroutines for performing message handling. The manner in which they have been written allows them to be easily incorporated into a software program.



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