## ACRONYMS AND ABBREVIATIONS

ACI analog carrier interface ACKBLK acknowledgment-block

ALIT automatic line insulation test
AML automatic maintenance limit
AROB activate remote order buffer

ATP all-tests-pass
AUD audible circuit
BGNBLK begin-block
BGT background task
Bp bank pointer
BPF band pass filter

BOC Bell Operating Company

BOS build-out switch

CAROT centralized automatic reporting on trunks

CARSEL carrier select input CC central control

CCIS common-channel interoffice signaling

CCITT Comite Consultatif International Telegraphique et

Telephonique

CCS hundred call seconds
CDO community dial office
CGA carrier group alarm
CI carrier interface

CLKOUT clock out CODEC coder-decoder

CML channel maintenance list

CMLA channel maintenance list for audits

CNTL control

CPD central pulse distributor CRC cyclic redundancy check

CRC-CCITT cyclic redundancy check-Comite Consultatif Inter-

national Telegraphique et Telephonique

DAC digital-to-analog converter
DDD direct distance dialing
DDS digital data system

DIGROUP digital carrier group (twenty-four channels)

DIP dual in-line package
DLENA data link enable A
DLENB data link enable B
DLI data link interface
DN directory number

DSU data service unit

DSU-DP data service unit-data port

EIA Electronics Industries Association

ELS electronic loop segregator

ENDR end-ROB

EPROM erasable programmable read-only memory

ESS electronic switching system

FETL far-end test line

FITS failures in one billion hours

FO fan out
G-J grid-junctor
GS ground start

GSA ground start applique

HLSC high-level service circuit (same as USC)

HROB high-priority ROB IC integrated circuit

IGFET insulated gate field effect transistor

IHA hold current—side A
IHB hold current—side B
I<sup>2</sup>L integrated injection logic

INHIB inhibit signal
INT interrupt signal
I/O input/output
IT talk current
JCT junctor

JFET junction field effect transistor

LED light-emitting diode LEN line equipment number

LI line interface
LIU line interface unit
LLC line load control
LLN line link network

LREN line remote equipment number

LSI large-scale integration

LTD local test desk

LTDA local test desk applique

LTDRTS local test desk remote test system

MCC maintenance control center

MCEN memory chip enable

MDAC multiplying digital-to-analog converter

MLAP metallic line access port
MLT mechanized loop testing

MRDY memory ready

MSD miscellaneous scan and distribute

MSI medium-scale integration
MTBF mean time between failures

MTTF mean time to failure

MWRT memory write

NEBS New Equipment Building Systems nMOS n-channel metal oxide semiconductor

OIU office interface unit

PA power alarm

PAB peripheral address bus
PAM pulse-amplitude modulation
PBX private branch exchange

PCI processor controlled interrogator

PCM pulse-code modulation
PMC path memory channel
PMJ path memory junctor
PMR path memory remote

PNPN semiconductor crosspoints, described by the four

layers in the device

POB peripheral order buffer POTS plain old telephone service

PSPD permanent signal and partial dial

PU peripheral unit

PWC pulse-width controlled PWB printed wiring board

PUC/DL peripheral unit controller/data link

PUC DL/DCT peripheral unit controller data link/digital carrier

terminal

RAM random access memory

R-DIPs resistor-DIPs, used for level adjustments in channels

RCLK receive clock
RE read enable

REN remote equipment number

RLT remote line test

RMDN remote miscellaneous distributor number

RMSN remote miscellaneous scan number

RNDIS receive not disable

RNPCM receive not pulse-code modulation

ROB remote order buffer
ROH receiver off-hook
ROTL remote office test line
rp register pointer

RSB repair service bureau

RSCO remote switching system central office end (D4 unit)

RSS remote switching system

RT remote terminal
RTS remote test system
RWD receive window

SAB stand-alone buffer

SCC switching control center
SCR silicon controlled rectifier
SDN special directory number
SDLC synchronous data link control

SF single frequency sp stack pointer

SRI client buffer identifier SSI small-scale integration

SXS step-by-step SYSRD system read TAB test access bus

TCI T-carrier channel interface

TDCLK transmit data clock
TLN trunk link network
TNDATA transmit not data
TR transmit-receive

TRCC T-carrier restoration control center

TTL transistor-transistor logic

TTY teletypewriter
TWD transmit window
UCR universal call register

USC universal service circuit (same as HLSC)

UV ultraviolet

VCCS voltage controlled current source

WE write enable

WEPL weighted echo path loss
WPBLK write-protect block