

ACRONYMS AND ABBREVIATIONS

2FXO/GT	Two-wire Foreign Exchange Office End with Gain Transfer Unit
4EM	four-wire E and M
ACO	alarm cutoff
ACU	alarm control unit
A/D	analog-to-digital
AGC	automatic gain control
ALM DET	alarm detector
ALM DISAB	alarm disable
ALM	alarm
ANI	automatic number identification
APD	avalanche photodiode
ARFAL	digroup A frame alarm
ARPCM	digroup A receive pulse code modulation
AR	red alarm
ATPAM	digroup A transmit pulse amplitude modulation
ATPCM	digroup A pulse code modulation
AY	yellow alarm
B6ZS	binary six zero substitution
BIL	buried injector logic
BOC	Bell operating company
BRFAL	digroup B frame alarm
BRPCM	digroup B pulse code modulation
BRPCM	digroup B receive pulse code modulation
BTPAM	digroup B pulse amplitude modulation
CAD	computer-aided design
CBIC	complementary bipolar integrated circuit
CCIS	common-channel interoffice signaling
CGA	carrier group alarm
CH	channel
CMOS	complementary metal oxide semiconductor
CO	central office
CODEC	coder-decoder
COT	central office terminal
CSU	channel service unit
CTU	channel test unit
CU SIM	channel-unit simulator
CU	channel unit
CUT	channel under test
D4MB	D4 Maintenance Bank
DAC	digital-to-analog converter
DACS	digital access and cross-connect system

D/A	digital-to-analog
dBrnC0	dB above reference noise, measured with C-message weighting at 0 transmission level point
dBTL	decibels-transmission level
DCLK	destuffed clock
DDS	Digital Data System
DEMUX	demultiplexer
DF	distribution frame
DIF	digital interface frame
DIP	dual in-line package
DLU	data link unit
DS1	digital signal one
DS0	digital signal zero
DSA	digital serving area
DSU	data service unit
EBES	Electron-Beam Exposure System
EEE	electronic equipment enclosure
EFS	error-free seconds
EIA	Electronic Industries Association
EQL	equalizer
ESS	electronic switching system
FET	field-effect transistor
FF	flip-flop
FL	fault locate
FL/OW	fault locate/order wire
FS	signaling, frame signal pattern
FT	terminal frame signal pattern
HIC	hybrid integrated circuit
IC	integrated circuit
IIL (I ² L)	integrated injection logic
ITS	integrated timing supply
JFET	junction field-effect transistor
LB	loopback code
LBO	line build-out network
LCIE	lightguide cable interconnection equipment
LC	inductor-capacitor
LCLK	loop clock
LED	light-emitting diode
LIU	line interface unit
LL	line loop
LOSP	loss of pulse code modulation
LPCM	loop pulse code modulation
LPF	low-pass filter
LSB	least significant bit

LSI	large-scale integration
LSL	logic simulation language
LSU	line switching unit
LT	loop terminal
LTS	local timing supply
MB ALM	maintenance bank alarm unit
MBTS	maintenance bank test sets
MC	multichannel
MCLK	multiplexer clock
MEM	memory
MLT	mechanized loop testing
MON	monitor
MOS	metal oxide semiconductor
MTS	Message Telephone Service
MUX	multiplexer
NEBS	New Equipment Building System
NMOS	n-channel MOS
NRZ	nonreturn to zero
NTS	nodal timing supply
OCUDP	office channel unit dataport
OCU	office channel unit
OIU	office interface unit
OOF	out-of-frame
ORU	office receiving unit
OS	out of service
OTU	office transmitting unit
PAM	pulse amplitude modulation
PCM	pulse code modulation
PCU	power converter unit
PDU	power distribution unit
PGTC	Pair Gain Test Controller
PLL	phase-locked loop
PMOS	p-channel MOS
POTS	plain old telephone service
RCLK	read clock
RCLK	receive clock
RC	resistor-capacitor
RFA	receive frame signal for digroup A
RFB	receive frame signal for digroup B
RNB2	receive, negated bit 2
RNPCM	receive, negated pulse code modulation
RPAM	receive pulse amplitude modulation
RPCM	receive pulse code modulation
RT	remote terminal

RTV	room temperature vulcanization
RU	receive unit
SBC	standard buried collector
SCC	switching control center
S/D	signal-to-distortion
SDPO	2-wire sleeve dial pulse originating
SIC	silicon integrated circuit
SIP	single in-line package
SMAS	Switched Maintenance Access System
SR	stuff request
SSI	small-scale integration
SS	special service
STAR	standard tantalum active resonator
STC	serving test center
STS	secondary timing supply
SUM	summing
SU	synchronizing-desynchronizing unit
syndes	synchronizing-desynchronizing
T1FP	T1DM framing pattern
T6CLK	transmit 6-megahertz clock
TAU	time assignment unit
TCLK	transmit clock
TED	transmission equipment DIP
TNEN	transmit, negated enable
TOGA	transmit outgoing alarm
TPAM	transmit pulse amplitude modulation
TPCM	transmit pulse code modulation
TPD	trunk processing delayed
TPM	trunk processed memory
TP	trunk processing
TPU	trunk processing unit
TPW	trunk processing wink
TRU	transmit/receive unit
TSI	time-slot interchanger
TTL	transistor-transistor logic
TU	transmit unit
UTE	unitized bay
VCO	voltage-controlled oscillator
VCXO	voltage-controlled crystal oscillator
VFD	voice-frequency detector
VF	voice frequency
XO	crystal oscillator
ZS	zero suppression